

net=MOSI_SYSTEM:1
MOSI_SYSTEM_VIA

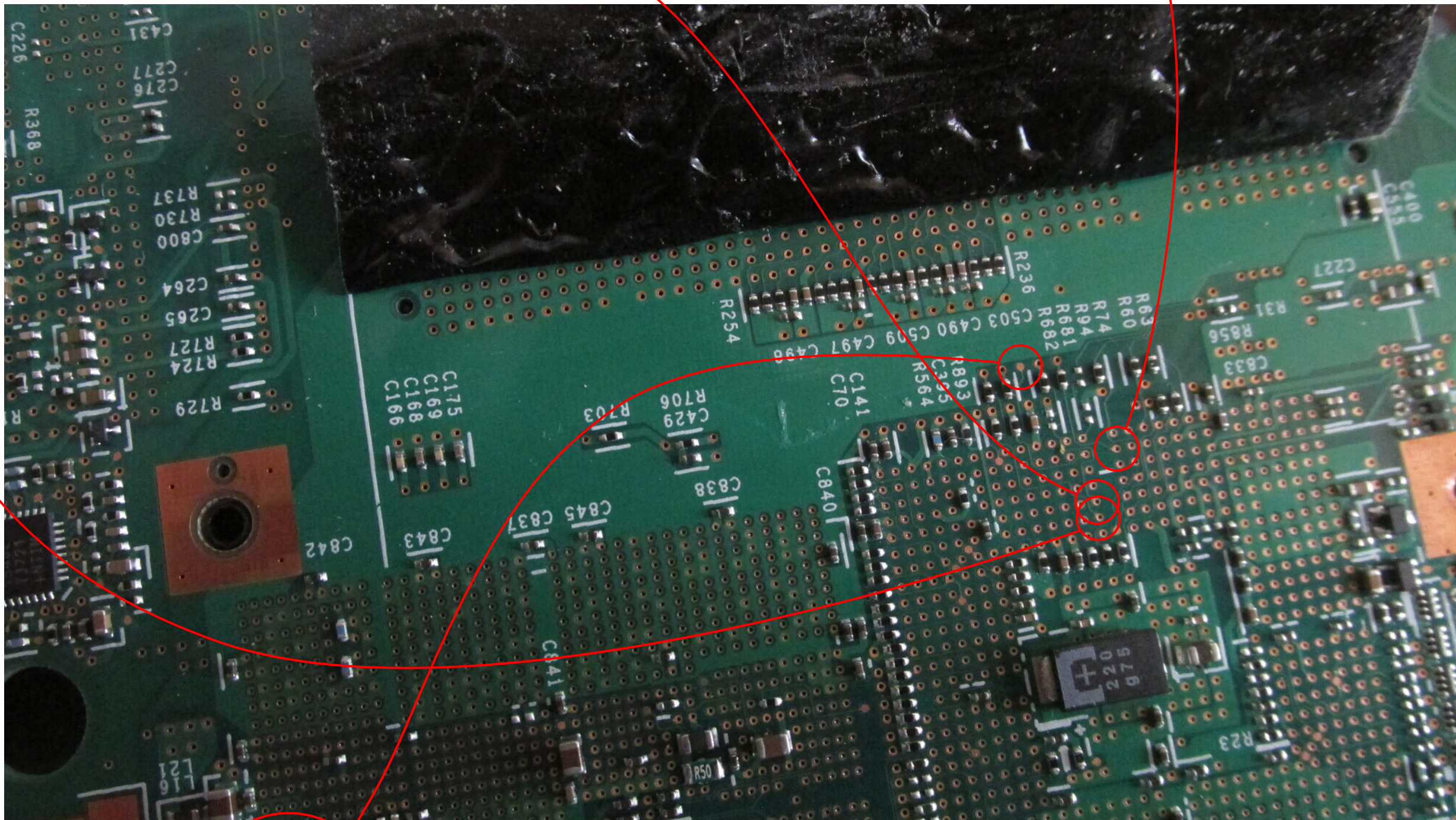
net=MOSI_SYSTEM:1
MOSI_SYSTEM

net=SCL_SYSTEM:1
SCL_SYSTEM_VIA

net=SCL_SYSTEM:1
SCL_SYSTEM

net= $\overline{\text{CS}}$ _SYSTEM:1
 $\overline{\text{CS}}$ _SYSTEM_VIA

net= $\overline{\text{CS}}$ _SYSTEM:1
 $\overline{\text{CS}}$ _SYSTEM



MISO_SYSTEM_VIA
net=MISO_SYSTEM:1

MISO_SYSTEM
net=MISO_SYSTEM:1

Zerocat Flash Access

Interfacing BIOS Chips in Hardware Protection Mode

[git://zerocat.org/zerocat/projects/zerocat-flash-access](https://zerocat.org/zerocat/projects/zerocat-flash-access)

TITLE

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REVISION:

v0.4.3-0-3c0d96e (master)

DRAWN BY:

kmx