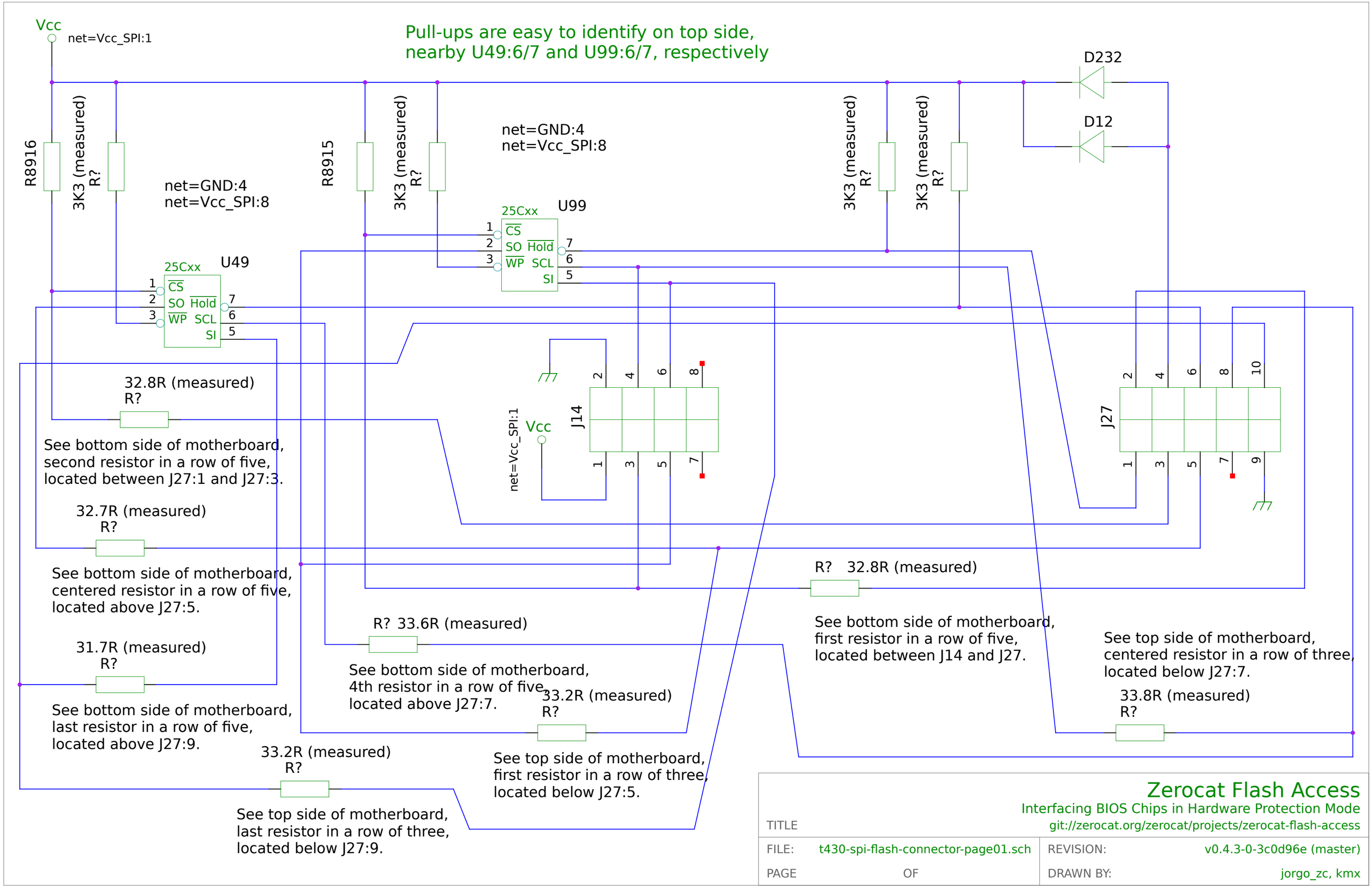
Vcc
net=Vcc_SPI:1
Pull-ups are easy to identify on top side,
nearby U49:6/7 and U99:6/7, respectively
D232
D12
R8916
3K3 (measured)
R?
net=GND:4
net=Vcc_SPI:8
R8915
3K3 (measured)
R?
net=GND:4
net=Vcc_SPI:8
3K3 (measured)
R?
3K3 (measured)
R?
25Cxx
U99
CS
SO
WP
Hold
SCL
SI
25Cxx
U49
CS
SO
WP
Hold
SCL
SI
32.8R (measured)
R?
See bottom side of motherboard,
second resistor in a row of five,
located between J27:1 and J27:3.
32.7R (measured)
R?
See bottom side of motherboard,
centered resistor in a row of five,
located above J27:5.
31.7R (measured)
R?
See bottom side of motherboard,
last resistor in a row of five,
located above J27:9.
33.2R (measured)
R?
See top side of motherboard,
last resistor in a row of three,
located below J27:9.
R? 33.6R (measured)
See bottom side of motherboard,
4th resistor in a row of five,
located above J27:7.
33.2R (measured)
R?
See top side of motherboard,
first resistor in a row of three,
located below J27:5.
net=Vcc_SPI:1
Vcc
J14
J27
R? 32.8R (measured)
See bottom side of motherboard,
first resistor in a row of five,
located between J14 and J27.
See top side of motherboard,
centered resistor in a row of three,
located below J27:7.
33.8R (measured)
R?
TITLE
Zerocat Flash Access
Interfacing BIOS Chips in Hardware Protection Mode
git://zerocat.org/zerocat/projects/zerocat-flash-access
FILE: t430-spi-flash-connector-page01.sch
REVISION: v0.4.3-0-3c0d96e (master)
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