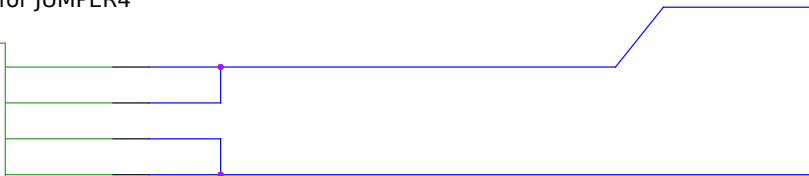


CONN9  
Plug for JUMPER4



Power Switch

S1

### Power Switch Cable

### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

FILE:

chipflasher-page09.sch

REVISION:

v2.0.5-0-619b5f407 (master)

PAGE

OF

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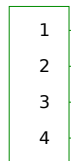
CONN6  
USB Plug Type A

Host

Chipflasher



CONN7  
USB Plug Type A



just power is used, no data wires necessary

CONN8



DC power jack plug,  
size 5.5mm,  
pin 2.1mm

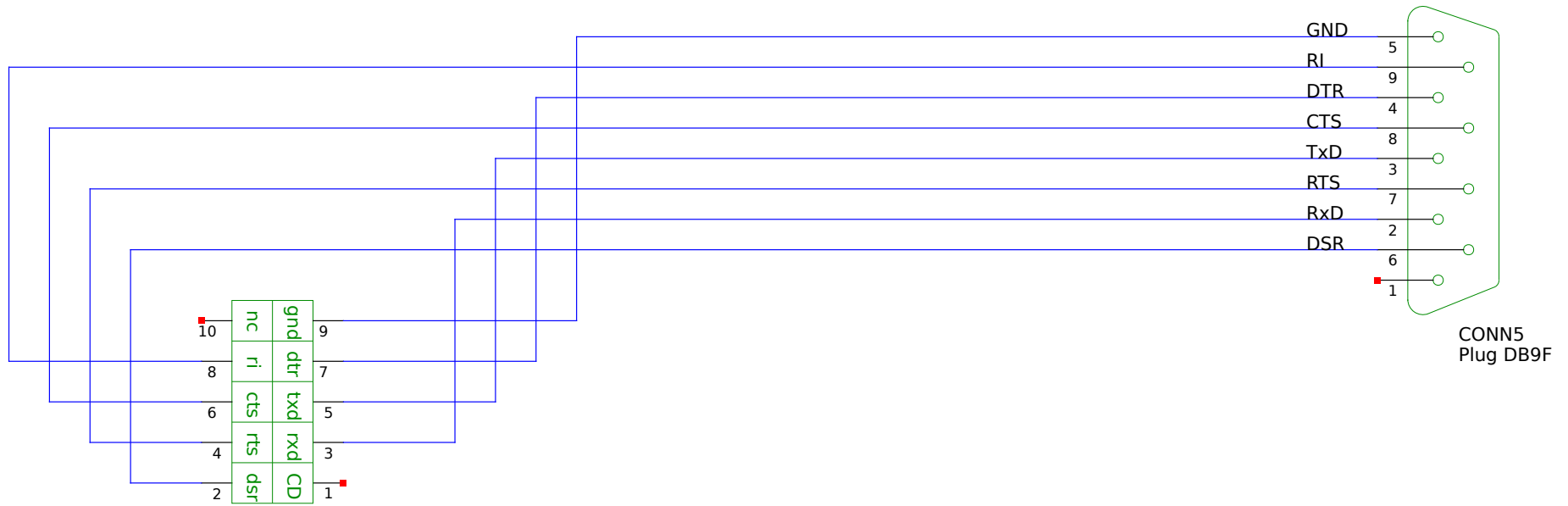
### Y-USB Power Cable

### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE			
FILE:	chipflasher-page10.sch	REVISION:	v2.0.5-0-619b5f407 (master)
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## RS232 cable, about 100cm long



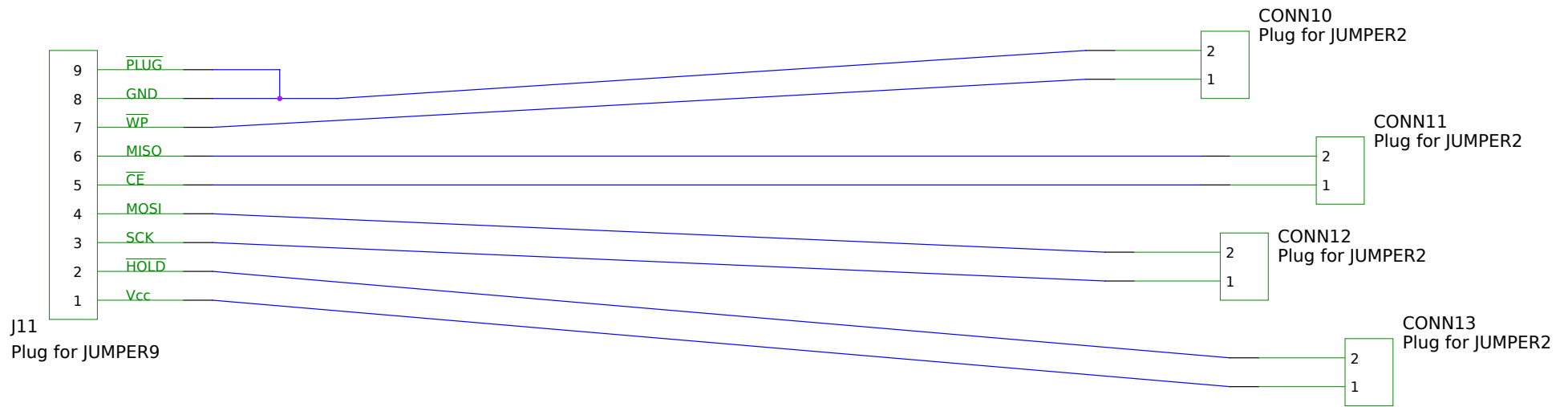
J10  
Plug for HEADER10\_2  
Header with DTE Pin Labels

Note:  
DTE = Data Terminal Equipment (Laptop)  
DCE = Data Communication Equipment (Chipflasher)

DTE pins in use:  
gnd = Signal Ground  
dtr = Data Terminal Ready (laptop is ready to send/receive, default reset line)  
rts = Request to Send (used as alternate reset line)  
txd = Transmit Data  
rxd = Receive Data

RS232 Cable		Zerocat Chipflasher	
TITLE		Flash free firmware, kick the Management Engine. <a href="https://zerocat.org/zerocat/projects/chipflasher">git://zerocat.org/zerocat/projects/chipflasher</a>	
FILE:	chipflasher-page11.sch	REVISION:	v2.0.5-0-619b5f407 (master)
PAGE	OF	DRAWN BY:	kmx

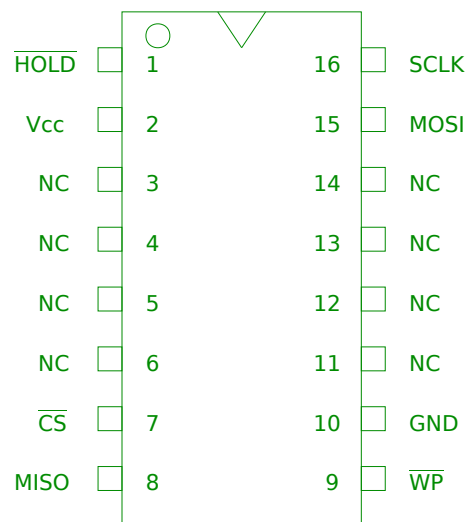
SPI Bus Cable, 20cm maximal length, don't bundle wires, use distance keepers



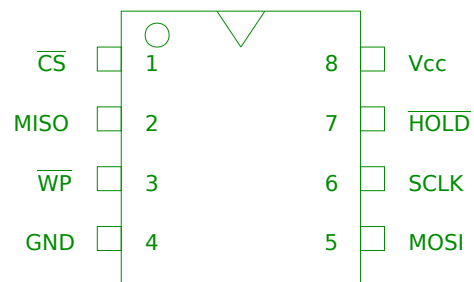
Universal Connectors for  
SOIC8 and SOIC16 Test Clips  
Attach to test-clip pins at handle!

SPI Bus Cable		Zerocat Chipflasher	
TITLE		Flash free firmware, kick the Management Engine. <a href="https://zerocat.org/zerocat/projects/chipflasher">git://zerocat.org/zerocat/projects/chipflasher</a>	
FILE:	chipflasher-page12.sch	REVISION:	v2.0.5-0-619b5f407 (master)
PAGE	OF	DRAWN BY:	kmx

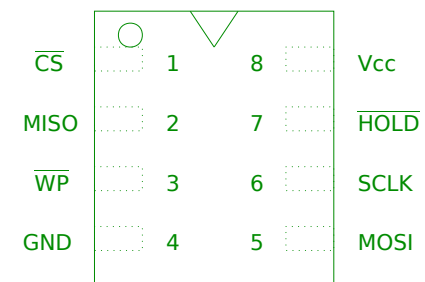
## SPI Flash Pinout, 16-PIN SOIC



## SPI Flash Pinout, 8-Pin SOIC or PDIP



## SPI Flash Pinout, 8-Pin WSON



### SPI Flash Pinouts

### Zerocat Chipflasher

Flash free firmware, kick the Management Engine.  
[git://zerocat.org/zerocat/projects/chipflasher](https://zerocat.org/zerocat/projects/chipflasher)

TITLE

FILE: chipflasher-page13.sch

REVISION: v2.0.5-0-619b5f407 (master)

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