R1
22K
RST_INHIBIT
U2 74HC32
RS232_RST
7432 Use 100nF cap for decoupling.
Vcc:14
GND:7
U3
5MHz
31 XO
30 XI
10 BOEn
11 RESn
P8X32A
U1 Vcc:12,32
GND:9,29
1) Use two 100nF caps for power pin decoupling, one left, one right.
2) If power falls below +2.7V, all pins become tri-state.
3) Pin RESn is pulled up through 5K when pin BOEn is low.
P0 1 PIN_CEn1
P1 2 PIN_CEn0
P2 3 PIN_MOSI
P3 4 PIN_WPn
P4 5 PIN_SCLK3
P5 6 PIN_SCLK2
P6 7 PIN_SCLK1
P7 8 PIN_SCLK0
P8 13 OSIM_NN
P9 14 LSTEST_PLUG
P10 15 PIN_PNP
P11 16 RST_INHIBIT
P12 17 PIN_HOLDn
P13 18 PIN_D3
P14 19 PIN_D2
P15 20 PIN_D1
P16 21
P17 22
P18 23
P19 24
P20 25
P21 26
P22 27
P23 28
P24 33 RS232_RST
P25 34 ADC_IN
P26 35 ADC_Calibration
P27 36 ADC_OUT
P28 37 I2C_SCL
P29 38 I2C_SDA
P30 39 RS232_TX
P31 40 RS232_RX
Rx from host / P31
Tx to host / P30
I2C SDA / P29
I2C SCL / P28
U2
4 5 6 7432
9 10 8 7432
12 13 11 7432
S7
S8
R10 4K7
C3 100nF RCY100 Blocking Cap for P8X32A.
C18 100nF RCY100 Blocking Cap for 74HC32.
Vcc
U9
SW DIP-6
R36 47K
Vcc

Controller with Reset Circuit

Zerocat Chipflasher
Flash free firmware, kick the Management Engine.
git://zerocat.org/zerocat/projects/chipflasher

TITLE			
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