Controller with onboard RAM

Zerocat Chipflasher

Flash free firmware. Kick the Management Engine.  
off/nowocz.org/zerocat/programmableflasher

C14 100nF RCY100  
Blocking Cap for 74HC32.

C12 100nF RCY100  
Blocking Cap for P8X32A.

C13 100nF RCY100  
Blocking Cap for P8X32A.

RESN is pulled up through 5k with B0EN is low.  
In order to disable Vdd, use two DG05s one per line right.  
If Vdd falls below +2.7V all pins become Hi-Z.

This chip is protected with socket.